

Notice of References Cited

Application/Control No.

09/945,555

Applicant(s)/Patent Under
Reexamination
GILGEN ET AL.

Examiner

Evan T. Pert

Art Unit

2829

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,215,187	04-2001	Ooto et al.	257/758
	B	US-6,166,941	12-2000	Yoshida et al.	365/63
	C	US-			
	D	US-			
	E	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Watanabe et al., "An Advanced Technique for Fabricating Hemispherical-Grained (HSG) Silicon Storage Electrodes", IEEE Transactions on Electron Devices, Vol. 42, No. 2, February 1995, pages 295-300.
	V	Gray & Meyer, Analysis and Design of Analog Integrated Circuits, 3 rd Edition, 1993, John Wiley & Sons, Inc., pages 168-170.
	W	Burns and Bond, Principles of Electronic Circuits, 1 st Edition, 1987, West Publishing Company, pages 390-396.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.